Getting started with EdgeLock[™] SE050 support package



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SECURE CONNECTIONS FOR A SMARTER WORLD



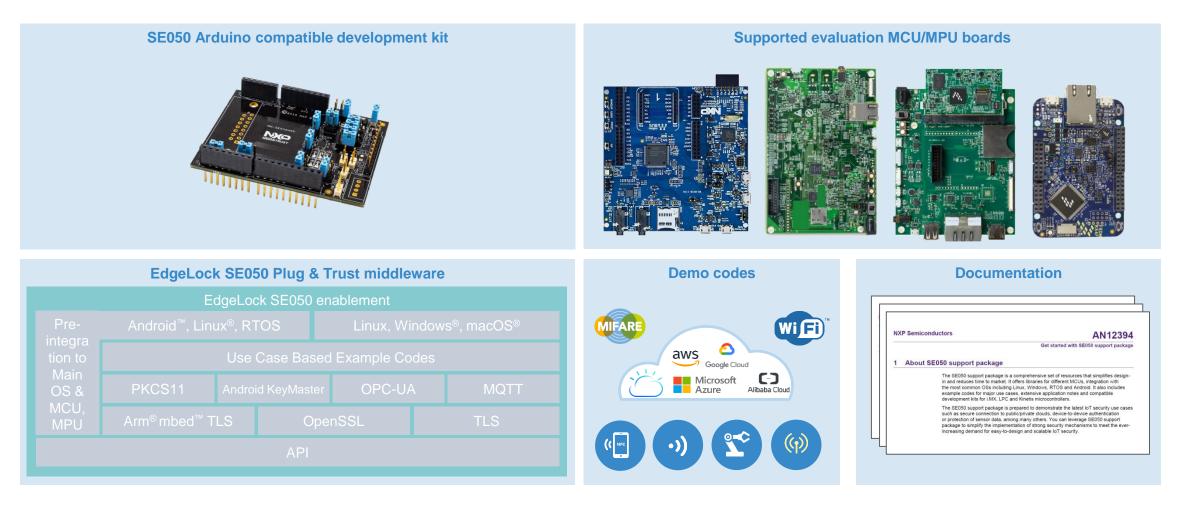
Agenda

- Support package overview.
- Get started with the FRDM-K64F*:
 - Get resources
 - -Install required SW and tools
 - Prepare hardware
 - Build SE050 Plug & Trust middleware
 - Run test examples
- pySSSCLI tool
- Evaluate use case examples.

* SE050 Plug & Trust middleware supports several MCUs / MPUs. FRDM-K64F is used as an example



EdgeLock SE050 support package





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EdgeLock SE050 product variants

Group	Feature	SE050 A 🧐	SE050 B 🥬	SE050 C 🧐	Dev Kit
	ECDSA	Yes	No	Yes	Yes
	ECDH	Yes	No	Yes	Yes
ECC algorithms	ECDHE	Yes	No	Yes	Yes
	ECDAA	No	No	Yes	Yes
	EDDSA	No	No	Yes	Yes
	ECC NIST (192 to 512 bit)	Yes	No	Yes	Yes
	ECC BrainPool (160 to 512 bit)	Yes	No	Yes	Yes
ECC curves	Koblitz (160 to 256 bit)	Yes	No	Yes	Yes
	Montgomery curve25519	No	No	Yes	Yes
	Twisted Edwards (for Ed25519)	No	No	Yes	Yes
RSA	RSA (up to 4096 bit)	No	Yes	Yes	Yes
Summotrio	(T)DES	Yes	Yes	Yes	Yes
Symmetric	AES (128-256 bit)	Yes	Yes	Yes	Yes
	TLS KDF, TLS PSK	Yes	Yes	Yes	Yes
Koy derivation	MIFARE DESFire KDF	No	No	Yes	Yes
Key derivation	WiFi KDF (PBKDF2)	Yes	Yes	Yes	Yes
	OPC_UA KDF	Yes	Yes	Yes	Yes
	I ² C slave	Yes	Yes	Yes	Yes
Interfaces	I ² C master	No	No	Yes	Yes
	ISO/IEC14443	No	No	Yes	Yes

OM-SE050ARD



OM-SE050ARD uses the SE050 Type C configuration





Get started with EdgeLock SE050 support package



How do I get familiar with support package contents? AN12394 - Get started with SE050 support package

How do I get started?

AN12396 - Quick start guide with Kinetis K64 AN12397 - Quick start guide with i.MX6UltraLite AN12542 - Quick start guide with LPC55S69* AN12398 - Quick start guide to SE050 Visual Studio projects AN12450 - Quick start guide with i.MX RT1050* AN12570 - Quick start guide with Raspberry Pi*

How do I get familiar with OM-SE050ARD dev kit? AN12395 - OM-SE050ARD hardware overview

* Will be published soon. Contact NXP if you need an early version.



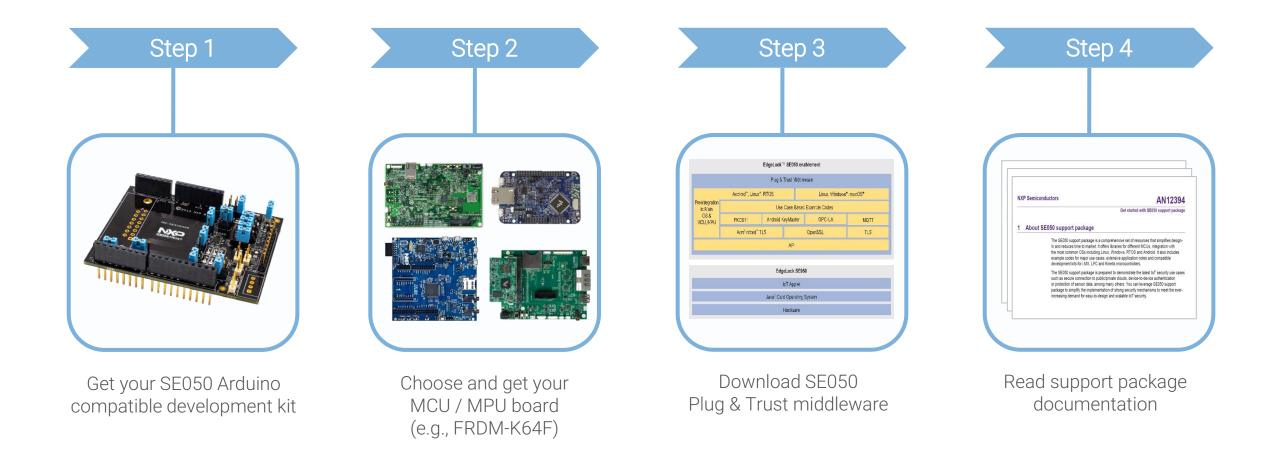
FRDM-K64F is used as an example to run your first demo project. The same demo projects are available for each supported MCU / MPU.



Get resources



Get EdgeLock SE050 support package resources







Get your SE050 Arduino compatible development kit

Follow 🛛 🗲

OM-SE050ARD: SE050 Arduino[®] Compatible Development Kit

Overview Specifications Buy Documents and Software

Overview

The OM-SE050ARD is the flexible and easy-to-use development kit for the EdgeLockTM SE050 Plug & Trust product family. It can be used in various ways for example via the Arduino interface compatible to any board featuring an Arduino compatible header, including many I.MX, LPC and Kinetis[®] boards, or via a direct f²C connection.

This kit allows evaluation of the SE050 product family features and simplifies the development of secure IoT applications.

More information can be found in the respective Application Note AN12395.







Sign-in with your account at the NXP website.

Scroll down and click on **Buy direct** button.





Fill-in your shopping basket and place your order.

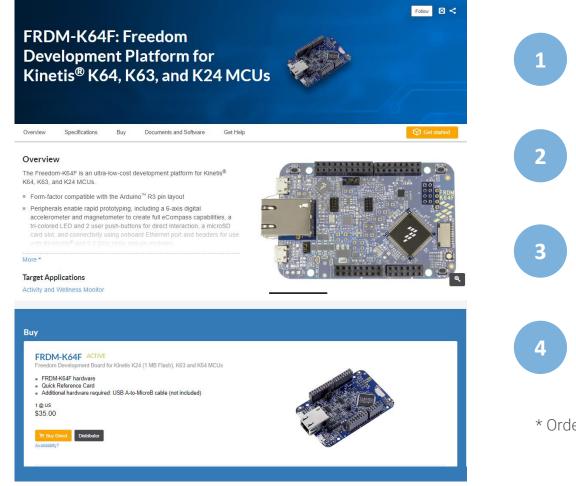
* Ordering is also possible via NXP distributors.





Go to https://www.nxp.com/products/:OM-SE050ARD

Get your MCU/MPU board



*FRDM-K64F used as an example. EdgeLock SE050 supports several MCU/MPUs

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Go to the MCU/MPU board webpage. https://www.nxp.com/products/:FRDM-K64F

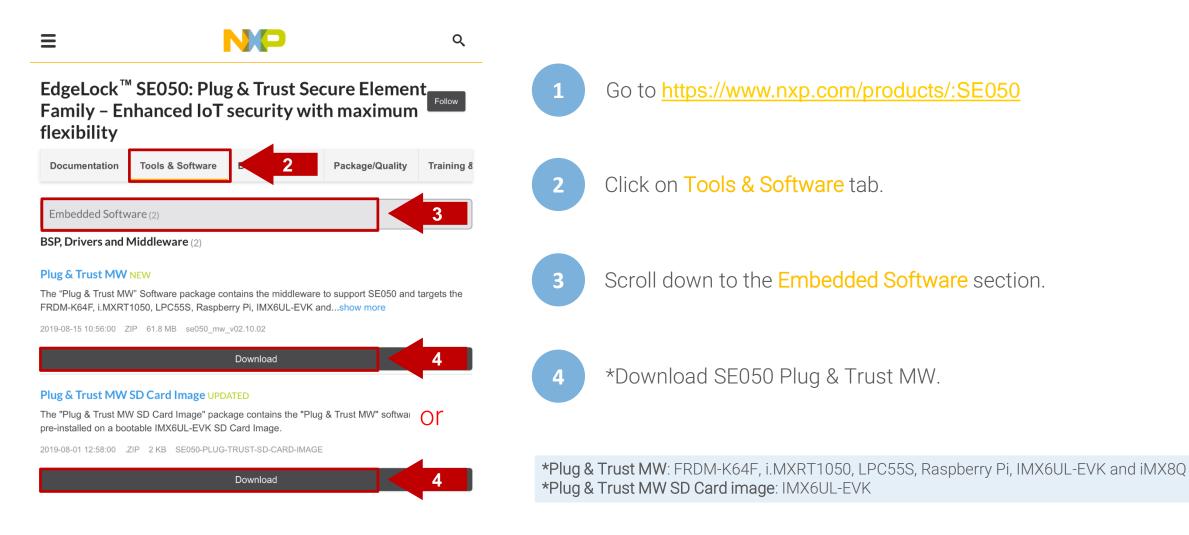


Fill-in your shopping basket and place your order.

* Ordering is also possible via NXP distributors.



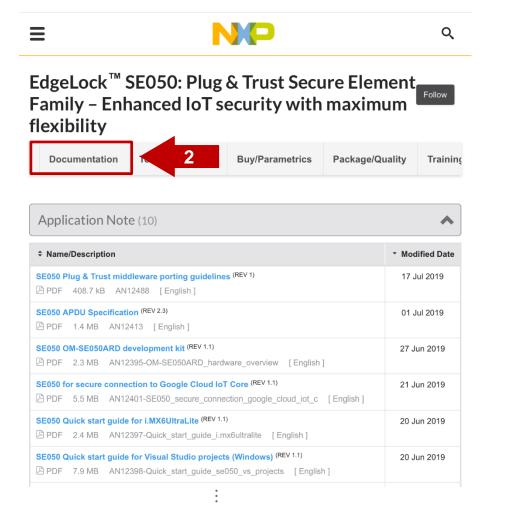
Get latest version of SE050 Plug & Trust middleware





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Get latest version of SE050 Plug & Trust documentation

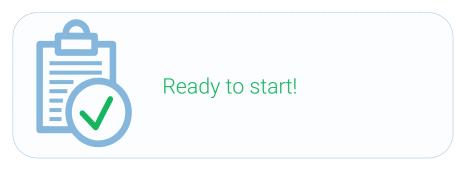




Click on Documentation tab.

3

Find your document within Application Note, Brochure, User Guide page sections , etc.







Install required SW and tools



Required software and tools



Install the following software tools in your development PC

CMake

An open-source, cross-platform tool designed to build, test and package software

Python 2.7 32-bit version A programming language used to generate scripts that facilitate operation with the MW.

MCUXpresso

A free-of-charge, easy-to-use IDE for Kinetis and LPC MCUs, and i.MX RT processors





Install CMake

🙏 CMake

About v Resources v Developer Resources v Down

Purchase support

Buy the book

Join the mailing list

CMake success stories

Attend a training course

KITWARE IS HIRING

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Get the Software

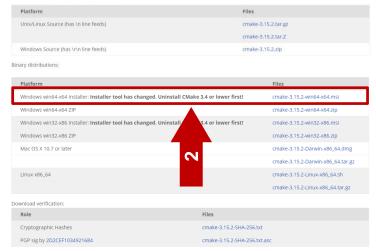
You can either download binaries or source code archives for the latest stable or previous release or access the current development (aka nighty) distribution through Git. This software may not be exported in violation of any U.S. export laws or regulations. For more information regarding Export Control matters please go to https://www.kitware.com/egal.

Stay updated

Kitware provides training sessions on CMake on a regular basis. If you are interested, please register, Kitware provides support for your CMake project such as migration from other tools to CMake, auditing of existing CMake-based project and training. You can always contact kitware for more information regarding CMake. If you want to get regular updates or more information regarding CMake services please leave us your email:

Latest Release (3.15.2)

The release was packaged with CPack which is included as part of the release. The shiftes are self extracting gziped tar files. To install a shifter run it with /bin/sh and follow the directions. The OS-machine Largz files are gziped tar files of the install tree. The OS-machine LarZ files are compressed tar files of the install tree. The tar file distributions can be untared in any directory. They are prefixed by the version of CMake. For example, the Linuxx86,64 tar files is all under the directory cmake-Linux-x86,64. This prefix can be ermoved as long as the share, bin, man and doc directories are moved relative to each other. To build the source distributions: unpack them with zip or tar and follow the instructions in Readme.txt at the top of the source tree. See also the CMake 31.5 Relaxes Notes. Source distributions:



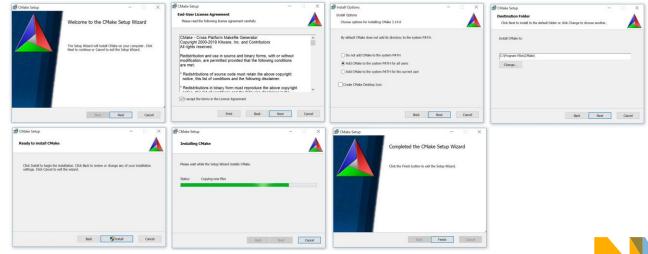
Go to https://cmake.org/download/



Scroll down and select your binary distribution.

3

Follow the setup wizard until the installation is completed.







Install Python

 → python[™]
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Python 2.7.15

Release Date: May 1, 2018

Python 2.7.15 is a bugfix release in the Python 2.7 series.

Note:

Attention macOS users: as of 2.7.15, all python org macOS installers ship with a builtin copy of OpenSSL. Additionally, there is a new additional installer variant for macOS 10.9+ that includes a built-in version of Tcl/Tk 8.6. See the installer README for more information.

Full changelog for 2.7.15rc1

Full changelog for changes between 2.7.15rc1 and 2.7.15

Files

Version	Operating System	Description	MD5 Sum	File Size	GPG
Gzipped source tarball	Source release		045fb3440219a1f6923fefdabde63342	17496336	SIG
XZ compressed source tarball	Source release		a80ae3cc478460b922242f43a1b4094d	12642436	SIG
macOS 64-bit/32-bit installer	Mac OS X	for Mac OS X 10.6 and later	9ac8c85150147f679f213addd1e7d96e	25193631	SIG
macOS 64-bit installer	Mac OS X	for OS X 10.9 and later	223b71346316c3ec7a8dc8bff5476d84	23768240	SIG
Windows debug information files	Windows		4c61ef61d4c51d615cbe751480be01f8	25079974	SIG
Windows debug information files for 64-bit binaries	Windows		680bf74bad3700e6b756a84a56720949	25858214	SIG
Windows help file	Windows		297315472777f28368b052be734ba2ee	6252777	SIG
Windows x86-64 MSI installer	Windows	for AMD64/EM64T/x64	0ffa44a86522f9a37b916b361eebc552	20246528	SIG
Windows x86 MSI installer	Windows		023e49c9fba54914ebc05c4662a93ffe	19304448	SIG





Go to https://www.python.org/downloads/release/python-2715/



Download Python v.2.7.15 32-bit version.

3

Follow the setup wizard until the installation is completed.



Make sure you download Python v2.7.15 32 bit version. Python v3 is not yet supported and neither is the 64 bit





Install MCUXpresso



Overview

The MCUXpresso IDE brings developers an easy-to-use Eclipse-based development environment for NXP® MCUs based on Arm® Cortex®-M cores, including LPC and Kinetis® microcontrollers and i.MX RT crossover processors. The MCUXpresso IDE offers advanced editing, compiling and debugging features with the addition of MCU-specific debugging views, code trace and profiling, multicore debugging, and integrated configuration tools. The MCUXpresso IDE debug connections support Freedom, Tower[®] system, LPCXpresso, i.MX RT, and your custom development boards with industryleading open-source and commercial debug probes from NXP, P&E Micro[®], and SEGGER[®].

2 User Guide Download MCUXpresso-IDE Block Diagram MCUXpresso IDE Eclipse Framework for C/C++, extendible with many plugins Integrated MCUXpresso Config Topis - Pins, Clocks, Periphera Quickstan Panel Support fo SDK and Advanced Build Step SWO Tracel Profiling Cores New Project Wizard Linker and Memory Configuration FreeRTOS Kerne Awareness ISIS-DAP P&E SEGGER IDE 15 PUBLIC Training

Knowledge

Go to www.nxp.com/mcuxpresso/ide



Download MCUXpresso IDE v.11 installer.

3

Follow the setup wizard until the installation is completed.

MCUXpresso IDE

F	iles	License Keys Notes				O Download Help
Sho	w A	I Files =				3 Files
	+	File Description	\$ File Size	ŧ	File Name	\$
	+	mcuxpressoide-11.0.0 - Linux	812.6	MB	tmcuxpressoide-11.0.0_2516.x86_64.deb.bin	
	+	MCUXpressoIDE_11.0.0 - MAC	777.7	MB	MCUXpressoIDE_11.0.0_2516.pkg	
	+	MCUXpressoIDE_11.0.0 - Windows	735.4	MB	MCUXpressoIDE_11.0.0_2516.exe	

Download Selected Files

Please, make sure you allow the installation of the additional drivers required by MCUXpresso during the installation process



Build SE050 Plug & Trust middleware



Unzip SE050 Plug & Trust middleware

Create folder to unzip SE050 Plug & Trust middleware in C:*

🔚 Pictures 🛛 🖈 🔨	Name	Date modified	Туре	Size
🌗 Music	Intel	2/25/2019 4:12 AM	File folder	
Projects	nxp	3/7/2019 1:28 AM	File folder	
📓 Videos	PEMicro	3/7/2019 1:34 AM	File folder	
a OneDrive	PerfLogs	4/11/2018 4:38 PM	File folder	
	🣜 Program Files	3/11/2019 4:05 AM	File folder	
This PC	📜 Program Files (x86)	3/11/2019 3:28 AM	File folder	
3D Objects	Projects	3/11/2019 6:17 AM	File folder	
늘 Desktop	Python27	3/11/2019 4:53 AM	File folder	
B Documents	se050_middleware	3/11/2019 6:27 AM	File folder	
Downloads	Users	2/25/2019 5:06 AM	File folder	
Music	Windows	3/11/2019 3:38 AM	File folder	
Pictures	Recovery	2/25/2019 12:46 PM	Text Document	О КВ
Videos				
-				
Local Disk (C:)				
USB DISK (E:)				
USB DISK (E:)				(Contraction)

Unzip SE050 Plug & Trust middleware into a folder 2 named simw-top**

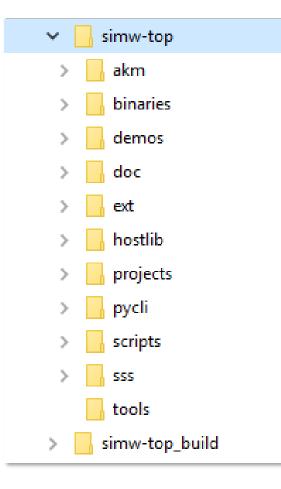
🖈 Quick access	^	Name	Date modified	Туре	Size
		akm	4/15/2019 2:10 PM	File folder	
Desktop	*	binaries	4/15/2019 2:10 PM	File folder	
🕂 Downloads	*	demos	4/15/2019 2:10 PM	File folder	
🔮 Documents	*	doc	4/15/2019 2:10 PM	File folder	
Pictures	*	ext	4/15/2019 2:10 PM	File folder	
j figures		hostlib	4/15/2019 2:10 PM	File folder	
ITE		nxp_iot_agent	4/15/2019 2:10 PM	File folder	
PTFs 2019			4/15/2019 2:10 PM	File folder	
			4/15/2019 2:10 PM	File folder	
topics		scripts	4/15/2019 2:10 PM	File folder	
MobileKnowledge		sss sss	4/15/2019 2:10 PM	File folder	
'his PC		lools	4/15/2019 2:10 PM	File folder	
-		CMakeLists.txt	4/11/2019 9:43 AM	Text Document	2 K
🧊 3D Objects		EULA.pdf	4/11/2019 9:43 AM	Adobe Acrobat D	131 K
📃 Desktop		PlugAndTrustMW.pdf	4/11/2019 9:43 AM	Adobe Acrobat D	1,978 K
🖆 Documents		README.First.txt	4/11/2019 9:43 AM	Text Document	2 K
👆 Downloads		A Third Party License.pdf	4/11/2019 9:43 AM	Adobe Acrobat D	316 K
Music		version_info.txt	4/11/2019 9:43 AM	Text Document	1 K
Pictures					

**simw-top: secure interface middleware top-level directory The naming is not strictly needed, but it is used in the rest of the presentation





SE050 Plug & Trust middleware folder structure



A software stack designed to facilitate the integration of NXP security ICs (A71CH, SE050) into your MCU or MPU.

- Akm: Android Keymaster
- Binaries: Pre-compiled FW for command line interface and VCOM software
- Demos: Demo code examples
- Doc: HTML documentation
- Ext: External libraries
- Hostlib: Source folder of the host library
- Projects: MCUXpresso projectsd
- Pycli: command line client
- Scripts: Helper compilation scripts
- Sss: SSS api source code
- Tools: Compile MW .dll library



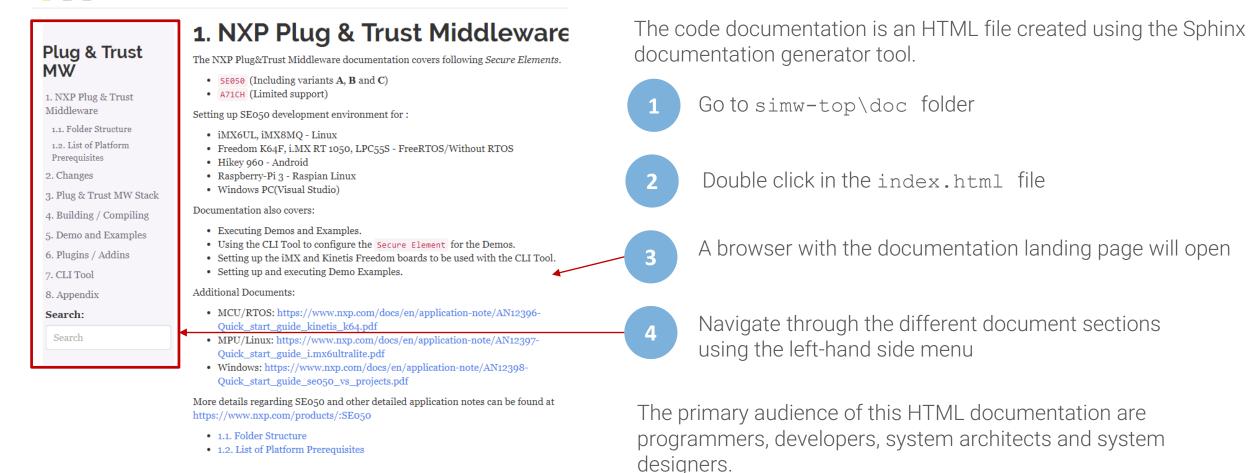


SE050 Plug & Trust code documentation

NР

v02.10.02

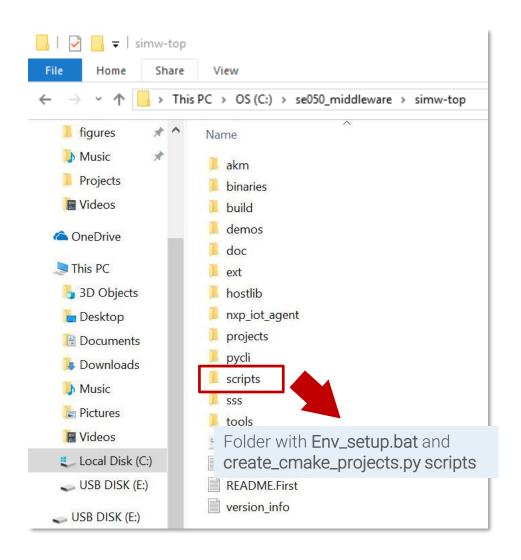
TOC → Page → « Contents 1.1. Folder Structure »







Build SE050 Plug & Trust middleware platform projects



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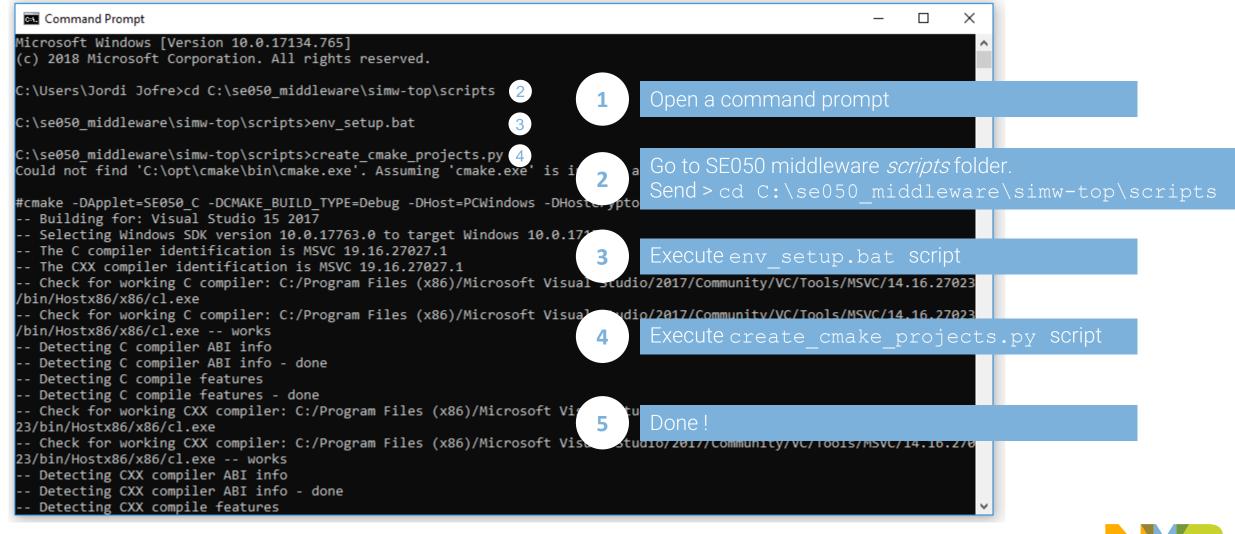
Training

SE050 Plug & Trust middleware includes scripts to automatically generate build projects:

Env_setup.bat Scans for installed toolchains / build environments, sets variables and adds them to the path: IDE: MCUXpresso tools folder JAVA_HOME: Java bin folder PYTHON_DIR: location of Python 2.7 CMAKE_DIR: location of CMake Creates a build folder for each detected buildable platform and the toolchains



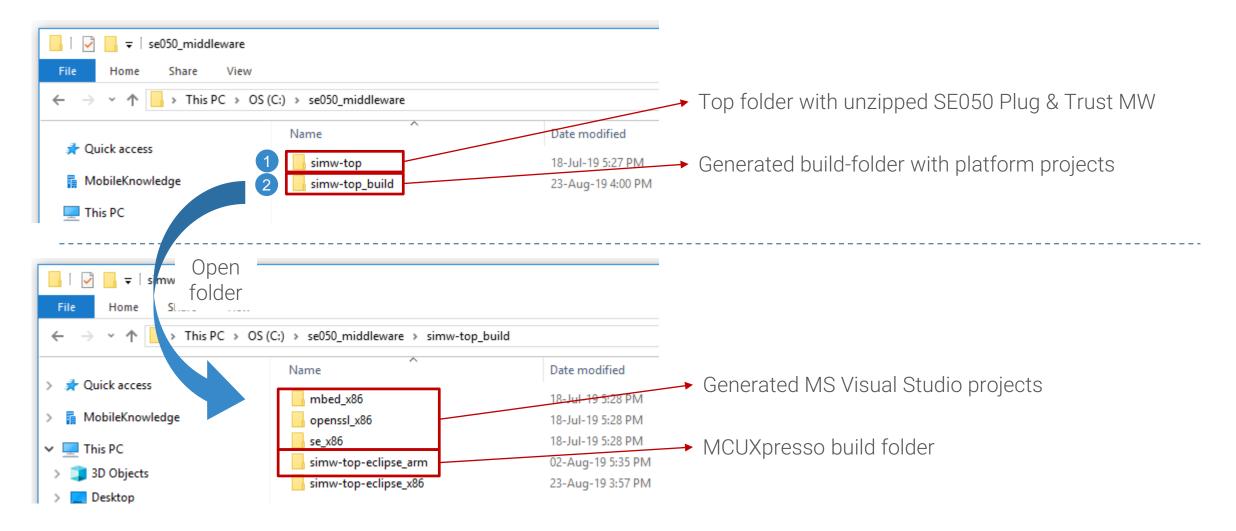
Build SE050 Plug & Trust middleware platform projects (II)







Check SE050 Plug & Trust build folder







Prepare HW



OM-SE050ARD kit contents









OM-SE050ARD board

A flexible and easy-to-use development kit for evaluation of the EdgeLock SE050 Plug & Trust product family.

OM-SE050ARD leaflet

A short quick start guide describing OM-SE050ARD headers and jumper configuration

Male connectors

Four spare male connectors in case your host MCU does not included soldered Arduino header pinout.

Connector for direct I2C connection

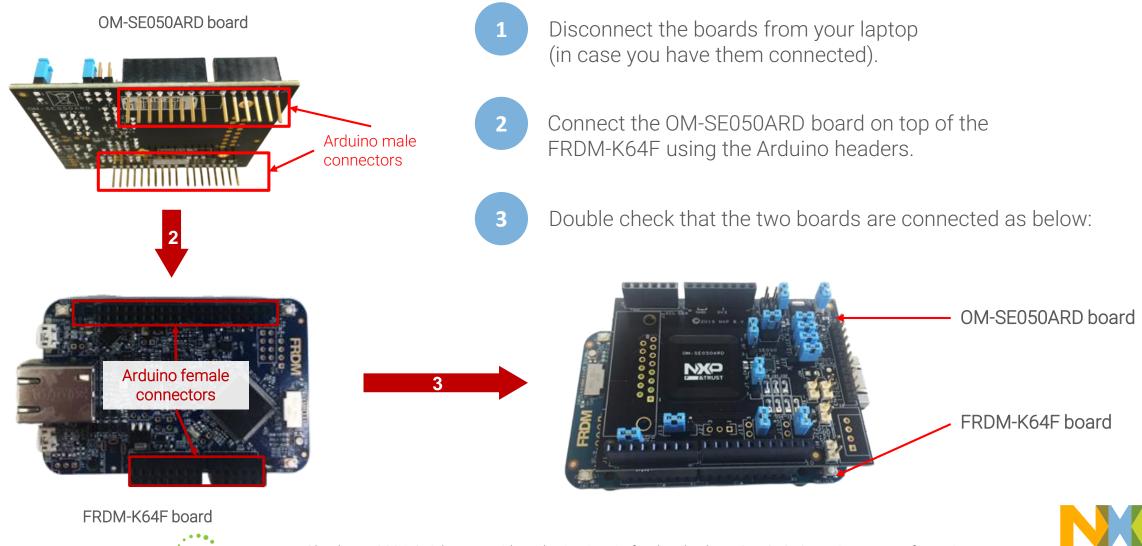


Hardware setup for FRDM-K64F

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Knowledge



*Check AN12396-Quick start guide with Kinetis K64 for details about OM-SE050ARD jumper configuration

PLUG & TRUST

Prepare MCUXpresso



Get FRDM-K64F SDK

SDK Dashboard	Select Development Board		
ENERAL	Search for your board or kit to get started.		
Select Board			
C Explore	Search by Name	Hardware Details	
	Search	Board	FRDM-K64F
DMINISTRATION		Device	MK64F12
Notifications	Select a Device, Board, or Kit		Cortex-M4F / 120MHz
Preferences	▼ Boards		1024 KB Flash
	▼ Kinetis		256 KB RAM
OWNLOADS	EVK-K32H844P Controlled access	Actions	
MCUXpresso IDE	FRDM-K22F		
	FRDM-K28F	Build MCUXpresso SD	к
MCUXpresso Config Tools	FRDM-K28FA	Explore selection with Pins	
-	FRDM-K32L3A6		
Dffline data	2 FRDM-K64F	Explore selection wit	th Clocks tool
ITERNAL	FRDM-K66F		
	FRDM-K82F		
Releases Information	N		
Hardware Releases	Name your SDK		
	SDK 2.6.0 FRDM-K64F		

Generate a downloadable SDK archive for use with MCUXpresso tools:



Go to https://mcuxpresso.nxp.com



Select FRDM-K64F board from the drop-down list



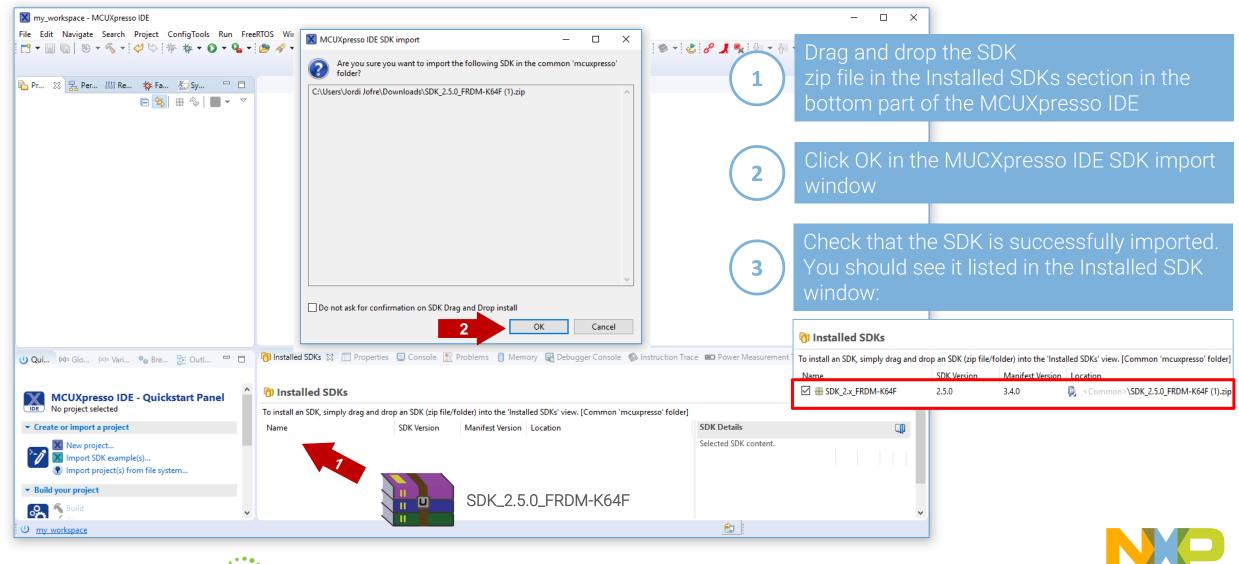


	This MCUXpresso SDK confi	guration is available for direct download
		Archive Name
4	4 Download SDK	SDK_2.5.0_FRDM-K64F (1)
		Don't use: <





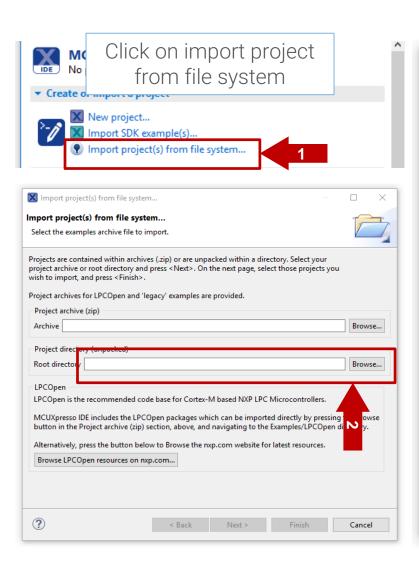
Import FRDM-K64F SDK into your MCUXpresso environment

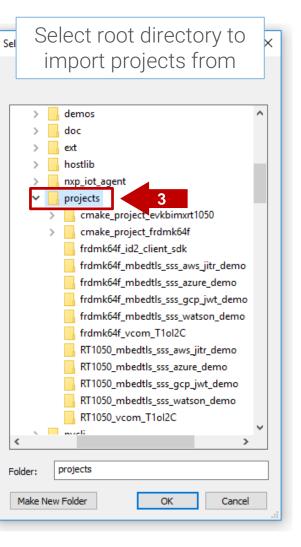


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Import project example





-					
🔀 Import pro		5		nd <u>untick</u> <i>ace</i> optior	
Import proje			тепсре		
Select a direct	ory to search for existi	ng Eclipse projects.			
Projects:					
cmake	_project_evkbimxrt105	i0 (C:\se050_middle	eware\simw-top	projects\cmake ^	Select All
	e_project_frdmk64f (C:\	-	1.1.2		4 t All
	n xrt1050_mbedtis_sss_ nxrt1050_mbedtls_sss_		-		
	nxrt1050_mbedtls_sss_		-		Refresh
evkbir	nxrt1050_mbedtls_sss_	watson_demo (C:\s	se050_middlewar	re\simw-top\pro	
	nxrt1050_vcom_T1ol20				
	64f_id2_client_sdk (C:\: 64f_mbedtls_sss_aws_ji	-			
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Options					
	ects into workspace	5			
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?		< Back	Next >	Finish	6

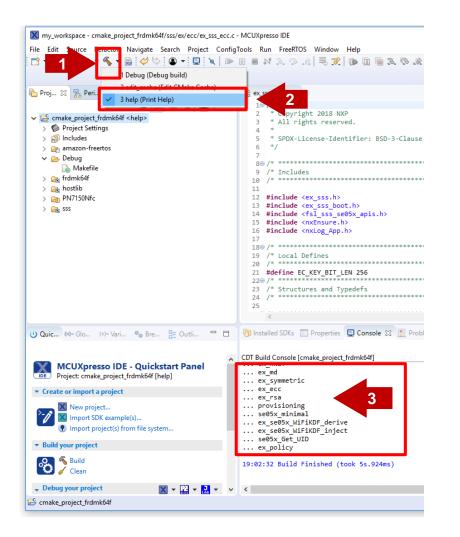
PLUG & TRUST



Run examples



SE050 Plug & Trust middleware test examples



The SE050 Plug & Trust MW comes with several test examples used to verify atomic SE050 security IC features.

- ... se05x_Get_Info: Gets SE050 system info (e.g. applet version)
- ... se05x_minimal: Gets free memory from SE050
- ... ex_ecc: Performs ECC signing and verify operation
- ... ex_rsa: Performs ECC signing and verify operation
- ... ex_symmetric: Performs AES encryption and decryption operation.
- ... ex_md: Performs Message Digest hashing operation
- ... ex_policy: demonstrate the use of policies for secure objects
- ... ex_hkdf: Performs HMAC Key derivation operation.

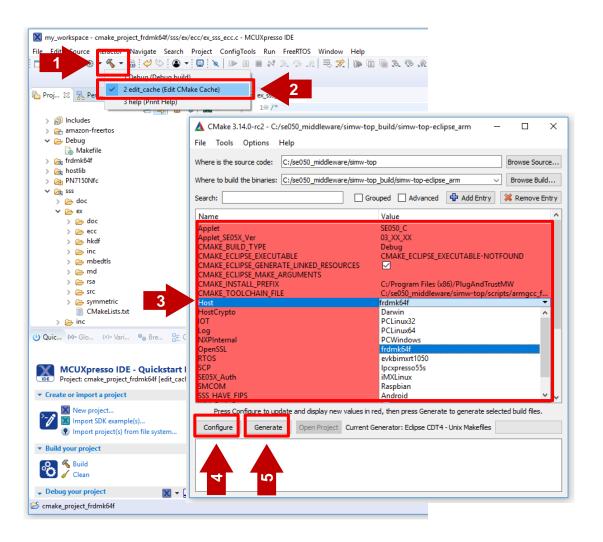
And more...

Described in SE050 Plug & Trust MW HTML documentation: simwtop/doc/demos.html





Edit CMake options



CMake configuration files are used to enable or disable several features, portability and setting flags to generate the build files for your platform and native build environment.



3

Click on the arrow on the "hammer" icon

Select 2 edit_cache

Use the CMake GUI window to change CMake options

Click on **Configure** button

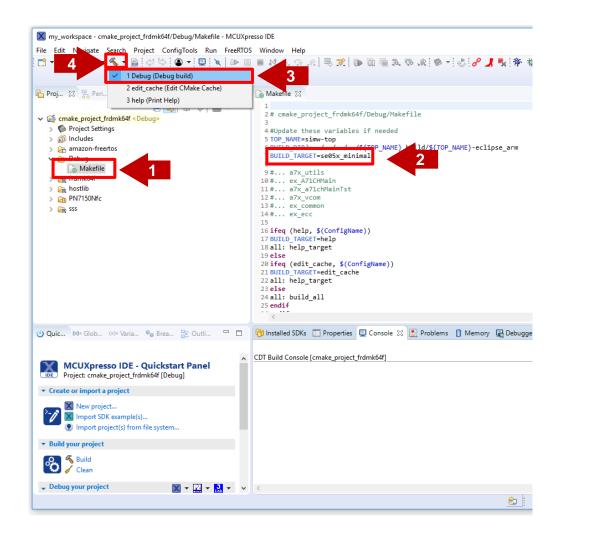
Click on Generate button



R



Define the test example to be executed



Select the se05x_minimal as the project to be executed. For that, follow the steps



Go to Debug folder and open the Makefile file.



Write the name of the project to be executed in the **BUILD_TARGET** variable (e.g. se05x_minimal)



Click on the arrow on the "hammer" icon



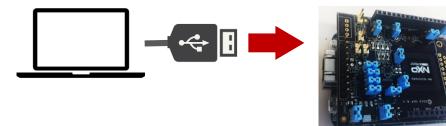




Run test example

1

Connect the FRDM-K64F board to your laptop



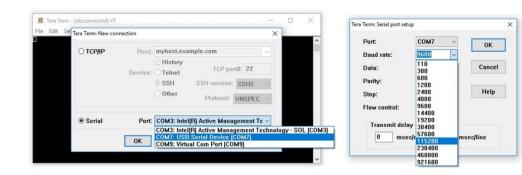
3

Click MCUXpresso Quickstart Panel Debug button

🔀 🌴 Debug	COT Build Console [cmake_project_framk64] Crimsprw.upressour_ofs.ve_zeegiuerprograms/community.mcuxpressor.coors.wrms2_10.s.e.z [14%] Built target amcom [23%] Built target amcom [74%] Built target amcom [31%] Built target amz, utils [92%] Built target sx_common [98%] Linking COX executable/././bin/se05x_minimal.axf se05x_minimal.axf >> e05x_minimal.bin [100%] Built target se05x_minimal
 Miscellaneous 	19:48:35 Build Finished (took 8s.610ms)
8 Edit project settings	
MCUXpresso Config Tools>>	~ <
(Ouick Settingers)	

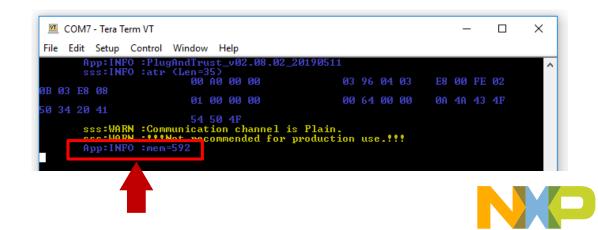
2

Configure TeraTerm





The project example is now running in. Check TeraTerm



PLUG &TRUS



pySSSCLI tool



pySSSCLI tool overview

	leware\simw-top\binaries\pySSSCLI≻ssscli [OPTIONS] COMMAND [ARGS]		
-	e interface for SE050		
ptions:			
version	se Enables verbose mode. Show the version and exit. Show this message and exit.		
Commands:			
cloud	A71CH specific commands (Not Implemented) Cloud Specific utilities. Open Session.		
disconnect erase	Close session. Erase ECC/RSA/AES Keys or Certificate (contents)		
get refpem	Generate ECC/RSA Key pair Get ECC/RSA/AES Keys or certificates Create Reference PEM/DER files (For OpenSSL Engine).		
	SE05X specific commands Set ECC/RSA/AES Keys or certificates Sign Operation		
verify	verify Operation		

C:\se050_middleware\simw-top\binaries\pySSSCLI>

- A command line tool able to insert keys and credentials inside the SE050.
- It is written in Python.
- It is meant for evaluation, development and testing phases.
- Supports complex provisioning scripts to be run on Windows, Linux, OS X and other embedded devices.
- Comes pre-compiled in SE050 Plug & Trust MW. Only requires to flash the VCOM software on the MCU board.

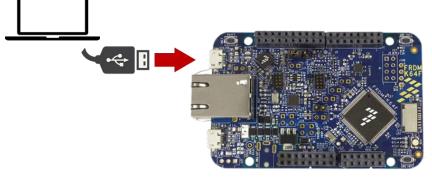
Further documentation about the commands: simw-top/doc/cli-tool.html



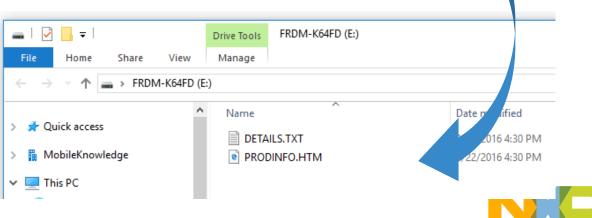


Flash FRDM-K64F with VCOM software

Unplug OpenSDA port 3 🔄 | 📝 📑 🖛 | binaries File Home Share View ← Name > 📌 Quick access pySSSCLI > MobileKnowledge a7x_vcom-SCI2C-evkbimxrt1050-A71XX.bin a7x vcom-SCI2C-frdmk64f-A71XX.bin > This PC a7x vcom-T1ol2C-evkbimxrt1050-SE050x.bin > 👝 USB Drive (D:) a7x_vcom-T1ol2C-frdmk64f-SE050x.bin Plug OpenSDA port Paste the binary file into the FRDM-K64F mass storage drive 4 🕳 | 📝 📙 🛨 Drive Tools







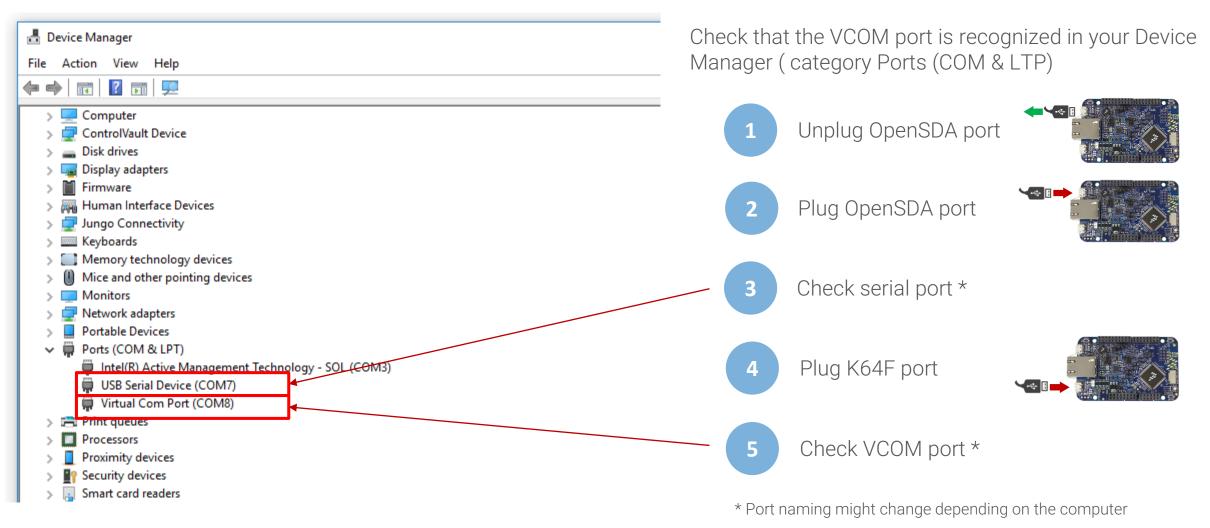
Copy The VCOM software binary from the simw-top\binaries

Date modified

6/5/2019 8:26 AM

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Flash FRDM-K64F with VCOM software (II)





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Use of the pre-compiled pySSSCLI

Command Prompt

Microsoft Windows [Version 10.0.17134.950] (c) 2018 Microsoft Corporation. All rights reserved.

C:\Users\Jordi Jofre>cd C:\se050_middleware\simw-top\binaries\pySSSCLI

C:\se050_middleware\simw-top\binaries\pySSSCLI>ssscli connect se050 vcom COM8

C:\se050_middleware\simw-top\binaries\pySSSCLI>ssscli se05x uid Opening COM Port '\\.\COM8'

sss:INFO :atr (Len=35)						
00 A0 00 00	03	96	04	03		
0B 03 E8 08						
01 00 00 00	00	64	00	00		
50 34 20 41						
54 50 4F						
sss:WARN :Communication channel is Plain.						
<pre>sss:WARN :!!!Not recommended for production use.!!!</pre>						
ss.se05x:04005001a8cd48e1a500f304250ec953	9000	9				
INF0:sss.se05x:Unique ID: 04005001a8cd48e1a500f304250ec9530000					90	
	00 A0 00 00 0B 03 E8 08 01 00 00 00 50 34 20 41 54 50 4F sss:WARN :Communication channel is Plain. sss:WARN :!!!Not recommended for production sss.se05x:04005001a8cd48e1a500f304250ec9530	00 A0 00 00 03 0B 03 E8 08 01 00 00 00 00 50 34 20 41 54 50 4F sss:WARN :Communication channel is Plain. sss:WARN :!!!Not recommended for production ss.se05x:04005001a8cd48e1a500f304250ec9530000	00 A0 00 00 03 96 0B 03 E8 08 01 00 00 00 00 64 50 34 20 41 54 50 4F sss:WARN :Communication channel is Plain. sss:WARN :!!!Not recommended for production use ss.se05x:04005001a8cd48e1a500f304250ec9530000	00 A0 00 00 03 96 04 0B 03 E8 08 01 00 00 00 00 64 00 50 34 20 41 54 50 4F sss:WARN :Communication channel is Plain. sss:WARN :!!!Not recommended for production use.!!	00 A0 00 00 03 96 04 03 0B 03 E8 08 01 00 00 00 00 64 00 00 50 34 20 41 54 50 4F sss:WARN :Communication channel is Plain. sss:WARN :!!!Not recommended for production use.!!!	

C:\se050_middleware\simw-top\binaries\pySSSCLI>



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Go to simw-top\binaries\pySSCLI folder cd C:\se050_middleware\simwtop\binaries\pySSSCLI



Open new connection Ssscli connect se050 vcom <COM PORT>



Send a command (e.g. read UID) Ssscli se05x uid





Evaluate EdgeLock SE050 use case examples



Evaluate EdgeLock SE050 use cases examples



Secure cloud onboarding

AN12401- SE050 for secure connection to GCP. AN12402- SE050 for secure connection to Azure IoT Hub. AN12404 - SE050 for secure connection to AWS IoT Core. AN12403- SE050 for secure connection to Watson IoT*.



Device-to-device authentication AN12399- SE050 for device-to-device authentication



Sensor data protection AN12401- SE050 for sensor data protection*

* Will be published soon. Contact NXP if you need an early version.

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Key injection Using the pySSSCLI tool or SE050 ease of use configuration

- Cloud account setup Create an account, create a logical device, register root CA / intermediate CA, etc.
- Run cloud onboarding demo Import project, change project settings, start the demo and check connection.



Evaluate EdgeLock SE050 use cases examples (II)



Secure access module AN12401- SE050 for secure access module*



Wi-Fi credential protection Application note under preparation



Late-stage parameter configuration Application note under preparation



Device ID for Blockchain Application note under preparation

Hardware setup & wiring How to connect the two FRDM-K64F boards

Software setup How to load the corresponding FW Run example Import project, change settings, start the demo



* Will be published soon. Contact NXP if you need an early version.



Last words



EdgeLock SE050 – a Root of Trust enabling new use cases

PLUG & TRUST Out-of-the-box Solution	Flagship 40nm architecture and CC EAL 6+ certified state of the art security concepts strongly protect against most recent attack scenarios. Additional features enable use cases to answer multiple application needs in IoT and especially industrial needs.				
	Enhanced security	Absolute flexibility			
NXP IoT security App Secure OS (JCOP 4) SE050 HW	 40nm Flagship Technology with IntegralSecurity 3.0 CC EAL 6+ VAN5 certified HW & OS RSA & ECC functionalities Future proof curves & higher key length Encrypted communication via SCP 	 Product family with multiple solutions for various new use cases Flexible applet with dynamic 50kB user memory Multiple interfaces - I2C Slave, I2C Master, ISO14443 Plug & Trust: Easy integration with multiple MCU/MPU platforms & OS, major Cloud integration 			
	 Symmetric ciphers for encryption/decryption 	 OPC-UA support & easy compliance for IEC62443 			
I ² C ISO/IEC I ² C slave 14443 master	Product website: <u>www.nxp.com/SE05(</u>				

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Development kit: www.nxp.com/ OM-SE050ARD





Time for Q & A





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- Mobile and cloud application development
- Secure e2e system design

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SECURE CONNECTIONS FOR A SMARTER WORLD

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