### MIGRATION TO EMVCO 3.0 NFC TECHNICAL III

PABLO FUENTES JULY 2019





SECURE CONNECTIONS FOR A SMARTER WORLD



## Agenda

- Introduction
- Test equipment
- EMV L1 Analog tests
- Interoperability tests
- NXP Debugging support
- NXP product portfolio



## Introduction



### Introduction Why EMV v3.0?

- With the appearance of contactless payments and card digitization, manufacturers enable new payment devices in a wide variety of form factors.
- In their v3.0, EMVCo updates their contactless specifications for POS terminals (PCD) to guarantee the correct operation with new devices in the market.
- POS terminals are now tested using 3 different reference antennas to verify the performance against different antenna sizes.
- Interoperability tests with several phones on the market were added and made mandatory from 2019Q1.





### Introduction EMV v3.0 Timeline

Schedule for the migration to EMV v3.0



 Interoperability tests mandatory for EMV v2.6

EMV 2.6b certification submission possible until Dec 31<sup>st</sup> 2019 (needs to be completed before Q2 2020)





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### Introduction Contactless Symbol

- Identifies the interface area where the user should tap his card.
- Contactless symbol marks the center of the landing plane.
- Minimum size: 13mm height x 22 mm width.
- Aspect ratio should be kept.













### Introduction **Operating volume**

Contactless Symbol is used to define an operating ٠ volume for the tests.

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Positions are expressed with the format (z, r,  $\varphi$ ) ٠

z	Height	r
0	0 mm	0
1	10 mm	1
2	20 mm	2
3	30 mm	
4	40 mm	

Radius	φ	Angle
0 mm	0	0
15 mm	3	π/2
25 mm	6	π
	9	3π / 2





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### Introduction TestPICC Positioning

• TestPICC should attack from the right side







## Test equipment



### Test equipment Test PICCs

EMV v2.6

EMV v3.0



PICC1 Class 1 16.1MHz

- For 3.0 tests are performed using 3 different PICCs instead of only 1
- New PICCs are tuned to 13.56 MHz
- Included a PICC Class 3 antenna



PICC2 Class 1 13.56MHz



PICC3 Class 3 13.56MHz



### **Test equipment** Load settings





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## **EMV L1 Analog Tests**



### **Power transfer tests** Differences – v2.6 vs v3.0





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### **Power transfer tests** EMV 2.6 vs 3.0 (TestPICC1 & 3)

z	z00	Min	Max
4	3.58	2.55	8.10
3	5.06	2.775	8.10
2	5.84	3.00	8.10
1	5.35	3.05	8.10
0	3.66	3.10	8.10

#### EMV 2.6

TestPICC1				
z	z00	Min	Max	
4	4.46	3.84	7.35	
3	5.37	4.02	7.35	
2	5.86	4.20	7.35	
1	5.62	4.25	7.35	
0	4.54	4.30	7.35	

**EMV 3.0** 

#### EMV 3.0 TestPICC3

z	z00	Min	Max
4	4.07	3.23	8.75
3	5.39	3.47	8.75
2	6.16	3.71	8.75
1	5.90	3.91	8.75
0	6.44	4.11	8.75

✓ Same device

✓ Same configuration





### **Power transfer tests** EMV 2.6 vs 3.0 (TestPICC2)

Z	<b>z00</b>	Min	Max
4	3.58	2.55	8.10
3	5.06	2.775	8.10
2	5.84	3.00	8.10
1	5.35	3.05	8.10
0	3.66	3.10	8.10

**EMV 2.6** 

EMV 3.0 TestPICC2				
Z	z00	Min	Max	
4	5.18	4.60	6.95	
3	5.47	4.60	6.95	
2	5.44	4.60	6.95	
1	5.45	4.60	6.95	
0	4.24	4.60	6.95	



4.60

4.60

6.95

6.95

5.45

4.88

1

0







### **Power transfer tests** Considerations

- Although voltage limits change in v3.0, different loads cause different measurements of voltage levels for the same RF power transmitted.
- Overall, new power transfer requirements are similar for TESTPICC1 and TESTPICC3. For TESTPICC2, device might require more power in positions at close distance.
- For migrations from EMV v2.6 to v3.0, it might be possible to fit new power transfer requirements just by changing DPC configuration (if supported).







### Wave shape tests Differences – v2.6 vs v3.0

- Same test cases as for EMV v2.6. Limits are the same, with slight changes in ringing, overshoot and undershoot test cases.
- In v3.0, all tests must be passed with the 2 different loadings (HLZ & LLZ) for all TESTPICCs. These two configurations aim at simulating the behavior of the reader against cards integrating chips with different load.
- Results show that EMV 3.0 specifications are more exigent in terms of waveform shape, especially when measured with TESTPICC2.
- Short distance positions seem to be the most challenging as the new TESTPICC loads present a low coupling with the PCD antenna in comparison with Reference PICC from v2.6.





### Wave shape tests Results with PNEV5180B (TestPICC 1)

Type A

z = 1cm

	Measured	Lower Limit	Upper Limit
t1	2.52	2.06	2.99
t2	1.41	0.52	2.52
t3	0.74	0	1.18
t4	0.35	0	0.44
Overshoot LLZ [%]	0	0	6.84
Overshoot HLZ [%]	0	0	6.84
Undershoot LLZ [%]	0	0	6.84
Undershoot HLZ [%]	0	0	6.84
ASK Mod. Depth [%]	99.37	95	100
Monotony	Pass	L LI	7
Ringing	Pass		

Measured	Lower Limit	Upper Limit
2.52	2.06	2.99
1.45	0.52	2.52
0.71	0	1.18
0.37	0	0.44
0	0	6.97
0	0	6.97
0	0	6.97
0	0	6.97
99.46	95	100
Pass	LLZ	
Pass		

Type B

z = 4cm

	Measured	Lower Limit	Upper Limit
Modulation Index [%]	12.8	9	14
tf	0.8	0	1.18
tr	0.64	0	1.18
Undershoot LLZ [%]	0.3	0	6.62
Undershoot HLZ [%]	0.3	0	6.62
Overshoot LLZ [%]	1.8	0	6.62
Overshoot HLZ [%]	1.8	0	6.62
Monotony falling	Pass	Ш	7
Monotony rising	Pass		

Measured	Lower Limit	Upper Limit
12.8	9	14
0.8	0	1.18
0.65	0	1.18
0.4	0	6.62
0.4	0	6.62
2.2	0	6.62
2.2	0	6.62
Pass		7
Pass	L	



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### Wave shape tests Results with PNEV5180B

### **Possible issues:**

- At close distances like 0 and 1 cm, the pick-up coil does not capture enough signal due to the low coupling with PCD antenna. This issue was already present in previous versions of the specifications, but it is accentuated with the tuning frequency and shape of the new antenas for v3.0.
- It is solved by using an external sniffer instead of the LETI Coil output of the TESTPICC antennas.
- Is important to notice that this issue is due to a measurement error, so any compensation to fit requirements under these conditions may lead to a bad behavior of the terminal in the field.

#### **TESTPICC LETI Coil**

*		Measured	Lower Limit	Upper Limit
	t1	2.89	2.06	2.99
	t2	2.54	0.52	2.89
	t3	0.18	0	1.18
	t4	0.14	0	0.12
	Overshoot LLZ [%]	17.74	0	9.25
	Overshoot HLZ [%]	17.74	0	15.22
	Undershoot LLZ [%]	0	0	9.25
	Undershoot HLZ [%]	0	0	15.22
	ASK Mod. Depth [%]	99.34	95	100
	Monotony	Pass		
	Ringing	Pass		

#### **External Sniffer**

	Measured	Lower Limit	Upper Limit
t1	2.88	2.06	2.99
t2	2.61	0.52	2.88
t3	0.29	0	1.18
t4	0.16	0	0.19
Overshoot LLZ [%]	5.17	0	8.77
Overshoot HLZ [%]	5.17	0	12.2
Undershoot LLZ [%]	0.59	0	8.77
Undershoot HLZ [%]	0.59	0	12.2
ASK Mod. Depth [%]	98.2	95	100
Monotony	Pass		
Ringing	Pass		





### LMA tests Differences – v2.6 vs v3.0

- For v3.0, LMA limits for minimum modulation are more 'relaxed' than for v2.6. However, limits for maximum modulation are slightly more restrictive.
- LMA limits remain the same for TestPICCs 1 and 2.
- For TestPICC 3 limits are less restrictive.

				EMV v3.0	
			TP1	TP2	TP3
PCD Nomi	nal Power	5.53	5.74		
0	MIN	5.5	5.5	5.5	6.0
/S1,pj	NOM	20	20		
	MAX	85	85	85	85
0	MIN	3.5	3.5	3.5	4.5
/S2,pl	NOM	20	20		
	MAX	40	40	40	33



## Interoperability tests



### **Interoperability tests** Test positions and orientation

- New tests included to verify performance against 8 different phones at L1 level.
- Applicable for 2.6b since Q1 of 2019.
- Testing positions:







## Interoperability tests Summary

- Tested with 8 different phones
- Motion requirements
  - Phone in testing position for 1.5 seconds to pass the test
- 37 positions for each orientation (74 positions in total)
- For every smartphone it must achieve:
  - > 50% successful transactions in z=0
  - > 83% successful transactions for all positions (including z=0)
    - The average score is weighted depending on how close the testing position is to plane z=0.







## **NXP** Optimization support



### NXP Optimization support Advantages

NXP offers a set of SW tools integrated in NXP NFC Cockpit to help debugging and optimizing terminal configuration for EMV L1 Analog test cases

- Intuitive GUI to configure and adapt IC settings
- Rx Matrix → Tool to test different receiver settings and find optimum values.
- AWG Control → To control and automatize Waveform Generator used for tests.
- SW to control mechanical TESTPICC positioning, using economic robot.

This provides semi-automatic register optimization for compatible NXP ICs





### NXP Optimization support NFC Cockpit







### NXP Optimization support NFC Cockpit

NVP NKP Cockpit v4.8.0 / VCOM_PN5180 @\\.\COM8 FW: v3.C EE: v153.0.0	- 🗆 X
Image: NXP NFC Cockpit v48.0 / VCOM_PN5180 @\\\COM8 FW: v3.C EE: v153.0.0      Registers/EEProm access    Operation      Register address:    0.22      Write    Register      Bit selection:    Image: Selection:      Image: Selection:    Image: Selection:	Reader LPCD DPC Test Signal Rx Matrix Scripting Extra Secondary FIW AWG Secondary Firmware Task List Load Secondary Firmware EMVCO Loop back (digital) Transaction Send A Transaction Send B 8:01:30)
VCOM_PN5180 @\\.\CON v  Close Port  Secure Upgrade  Soft Reset  ?	- INFO: Read Register RF_CONTROL_RX@0x22. Value=0x00000019





### NXP Optimization support DOBOT Magician



### **NXP FireArm Positioner to control DOBOT**

	Dobot Control	
	Device Seriel Numbers 0713 Device Name: 1 Version of Device Software: 2.6.1	17080831
f Template File	COM Port usedi	COM17
	Durnity COM Ports	
	Connected	Discognect
	Server Socket	
	Socket State: Listening	
	Socket Ports 7460	
	IP Addresser 127.0.0.1	
Move to goint	Last Message: Running on 3h 12	7.0.0.1. port 7480
• Move to gove Sign: 0.01	Last Mesage Running on 3h 12	7.6.4.1. port 7480
• Move to gard	Last Message Running on 3h 12 Direct Control	7.0.0.1. port 7480
• Move to gove	Leef Message Running on 3h 12 Direct Control E Editore Cymret	7.0.0.1. port 7460 Set Current ge Origin
• Move to gave Spec: 9.04	Leef Message Running on 3h 12  Direct Control  Setness Cyment  Move to Grigen	7.6.4.1. port 7460 Set Current yo Origin House to Current





### NXP Optimization support NXP Solution



### NXP Optimization support NXP Solution

### Provided by NXP

#### NXP NFC Cockpit:

- Drives PN5180, PN7462 or CLRC663 (+ derivatives)
- AWG
- RxMatrix

#### **NXP Firearm Positioner:**

- Allows to automate the EMV positions for testing
- Compatible with 'Dobot' (~2k€)







## NXP Product Portfolio for EMV



### NXP Product Portfolio for EMV PN5180 – Key Features







### NXP Product Portfolio for EMV PN5180 – DPC in detail







### NXP Product Portfolio for EMV PN5180 – ARC and AWC

- Adaptative Waveform Control (AWC) and Adaptative Receiver Control (ARC) allows you to dynamically configure and adjust parameters involved in waveform generation and reception.
- Using the different gears defined in the DPC, a correction can be applied to several parameters depending on the gear used.

	ad 1	Gear 0	Gain 3 HPCF 2	Rising time 4 Falling time 5
ance		Gear 1	Gain -1	
Dist	Lo	Gear 2		Rising time +1
		Gear 3	HPCF +1	Falling time +1





### NXP Product Portfolio for EMV PN7462 – Key Features

State-of-the-art reader solution on a single chip Contact and contactless interfaces with full MIFARE family support powered by an ARM Cortex-M0 core

All integrated although highly customizable 160/80kB Flash memory, USB, GPIOs, various host and master interfaces





**Faster time-to-market** Complete support package including NFC Forum compliant SW library and source code of typical applications



Smaller footprint at lower system BOM

Reducing system components and PCB by up to 50% in typical applications

NFC controller development kit OM27462CDKP





### NXP Product Portfolio for EMV CLRC663 *plus* – Key Features





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## NXP Product Portfolio for EMV Summary

- NXP plans to have qualified their family of NFC chips for payment by 2020
  PN5180 → Already qualified for EMV v3.0!
  PN7462 → In process
  CLRC66303 → In process
- New PN5190 designed specifically to meet EMV 3.0 requirements; Samples to be available before end of year
- Digital compliance is done.
- NFC Library is upgraded to support EMV 3.0 (From v05.19.00)





# More support





### **NXP** Relevant resources regarding POS

Certification	NXP support	End customer
EMVCo L1 contact analog	Application notes; demo board; Report from test house Customer schematic validation	Final device needs to be tested at a certified lab
EMVCo L1 contact digital	Application note; source code; ICS example; internal test report Support on NXP stack integration Support on EMV test suite errors	Final device needs to be tested at a certified lab
EMVCo L2 contact	Link to partners for stack ; Pre integration support if NXP L1 stack is used	Final device needs to be tested at a certified lab

Certification	NXP support	End customer	
EMVCo L1 contactless analog	Antenna design guide, loop back example; internal test report; demo board Antenna design support & RF support from CAS team	Final device needs to be tested at a certified lab	
EMVCo L1 contactless digital	Source code; application note ICS example; internal test report Support on NXP stack integration Support on EMV test suite errors	Final device needs to be tested at a certified lab	
EMVCo L2 contactless	Link to partners for stack ; Pre integration support if NXP L1 stack is used	Final device needs to be tested at a certified lab	





### MobileKnowledge Contact

We are your ideal **Engineering consultant** for any specific support in connection with your **EMV L1** approval process.

If you want to:

- Design a PCD L1, Mobile/Wearable L1 compliant device
- Design and optimize the performance of your contactless antenna
- Debug your device to make sure it is EMV L1 compliant
- Accelerate your time to market and avoid never-ending test processes

We have the tools and expertise to help you achieve EMV 3.0 compliance

Your trusted partner and expert design house for NFC technology

contact@themobileknowledge.com

themobileknowledge.com





### Get ahead with NXP's PN5180 Frontend - Design your POS terminal with EMVCo (L1) certification

Pablo Fuentes (Speaker) Angela Gemio (Host)

> Time for Q & A





Get ahead with NXP's PN5180 Frontend - Design your POS terminal with EMVCo (L1) certification

### Thank you for your kind attention!

Please remember to fill out our evaluation survey (pop-up)

Check your email for material download and on-demand video addresses

Please check NXP and MobileKnowledge websites for upcoming webinars and training sessions

http://www.nxp.com/support/classroom-training-events:CLASSROOM-TRAINING-EVENTS www.themobileknowledge.com/content/knowledge-catalog-0





### MobileKnowledge

MobileKnowledge is a team of HW, SW and system engineers, experts in **smart, connected and secure** technologies for the IoT world. We are your ideal **engineering consultant** for any specific support in connection with your **IoT** and **NFC** developments. We design and develop secure HW systems, embedded FW, mobile phone and secure cloud applications.

Our services include:

- Secure hardware design
- Embedded software development
- NFC antenna design and evaluation
- NFC Wearable
- EMV L1 pre-certification support
- Mobile and cloud application development
- Secure e2e system design

www.themobileknowledge.com mk@themobileknowledge.com







We help companies leverage the secure IoT revolution

